

## **Application Data Sheet**

### **Application Information**

Application Type::	Regular
Subject Matter::	Utility
Suggested Group Art Unit::	Not Yet Assigned
CD-ROM or CD-R?::	None
Sequence submission?::	None
Computer Readable Form (CRF)?::	No
Title::	<b>SELECTING DIE PLACEMENT ON A SEMICONDUCTOR WAFER TO REDUCE TEST TIME</b>
Attorney Docket Number::	524322001200
Request for Early Publication?::	No
Request for Non-Publication?::	Yes
Total Drawing Sheets::	3 (Formal)
Small Entity?::	Yes
Petition included?::	No
Secrecy Order in Parent Appl.?::	No

### **Applicant Information**

Applicant Authority Type::	Inventor
Primary Citizenship Country::	Israel
Status::	Full Capacity
Given Name::	Eitan
Middle Name::	
Family Name::	CADOURI
City of Residence::	Cupertino
State or Province of Residence::	California
Country of Residence::	United States of America
Street of mailing address::	937 Liberty Court
City of mailing address::	Cupertino

State or Province of mailing address:: California

Postal or Zip Code of mailing address:: 95014

### Correspondence Information

Correspondence Customer Number:: 20872

### Representative Information

Representative Customer Number:: 20872

### Domestic Priority Information

Application::	Continuity Type:	Parent Application::	Parent Filing Date::
This application	<i>An application claiming the benefit under 35 USC 119(e)</i>	60/453,859	03/12/03

### Foreign Priority Information

Country::	Application Number::	Filing Date::	Priority Claimed::
N/A			

### Assignee Information

Assignee name:: PDF Solutions, Inc.

Street of Mailing Address:: 333 West San Carlos, Suite 700

City of Mailing Address:: San Jose

State or Province of Mailing Address:: California

Postal or Zip Code of Mailing Address: 95110